

Overlapping Rules and Logic Variables in Functional Logic Programs

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FUNCTIONAL LOGIC LANGUAGES

Approach to amalgamate ideas of declarative programming

- efficient execution principles of functional languages (determinism, laziness)
- flexibility of logic languages (constraints, built-in search)
- avoid non-declarative features of Prolog (arithmetic, I/O, cut)
- combine best of both worlds in a single model (higher-order functions, declarative I/O, concurrent constraints)
- Advantages:
 - optimal evaluation strategies [JACM'00,ALP'97]
 - new design patterns [FLOPS'02]
 - better abstractions for application programming (GUI programming [PADL'00], web programming [PADL'01, PPDP'06])



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Minimal kernel language for FLP?



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[] ++ ys = ys  
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+ **logic variables** (expressive power)

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`last xs | ys ++ [x] == xs = x` where `x,ys` free



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+ **overlapping rules** (demand-driven search)

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insert e [] = [e]
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insert e (x:xs) = e : x : xs      -- overlapping
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insert e (x:xs) = x : insert e xs -- rules
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perm [] = []
perm (x:xs) = insert x (perm xs)

perm [1,2,3] ~> [1,2,3] | [1,3,2] | [2,1,3] | ...
```

= **functional logic languages**

Main result of this paper: **only one of these extensions is sufficient!**



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Datatypes (\approx admissible values): **enumerate all data constructors**

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data Bool      = True   | False
data Nat       = 0      | S Nat
data List      = []     | Nat : List
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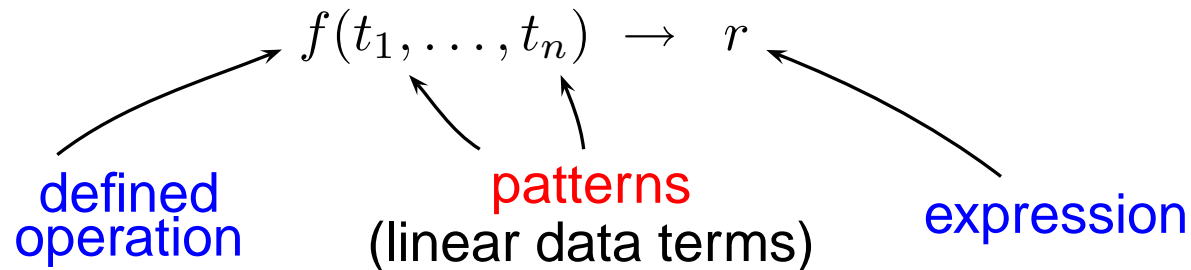
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Rewrite rules: define operations on values



```
add(0, y)      → y                positive(0)    → False
add(S(x), y)   → S(add(x, y))     positive(S(x)) → True
```

Extra variable: rule variable not occurring in left-hand side



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Inductively sequential term rewriting systems:

- no overlapping left-hand sides in rules
- operations inductively defined on datatypes

$\text{leq}(0, x)$	$\rightarrow \text{True}$	$\text{cond}(\text{True}, x)$	$\rightarrow x$
$\text{leq}(S(x), 0)$	$\rightarrow \text{False}$		
$\text{leq}(S(x), S(y))$	$\rightarrow \text{leq}(x, y)$	seven	$\rightarrow S(S(S(S(S(S(S(0)))))))$



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OIS: **overlapping** inductively sequential TRS:

allow rules with multiple right-hand sides: $l \rightarrow r_1 ? \dots ? r_k$

$$\text{parent}(x) \rightarrow \text{mother}(x) ? \text{father}(x)$$


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Main result: **OIS with rewriting** \iff **ISX with narrowing**



EVALUATION: REWRITING VS. NARROWING

Functional evaluation: (lazy) **rewriting**

$\text{add}(\text{S}(0), \text{S}(0)) \rightarrow \text{S}(\text{add}(0, \text{S}(0))) \rightarrow \text{S}(\text{S}(0))$



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Functional **logic** evaluation: **narrowing**: guess values + rewriting

$$\text{add}(x, S(0)) ::= S(S(S(0))) \rightsquigarrow \{x \mapsto S(S(0))\}$$



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$$\text{add}(x, S(0)) ::= S(S(S(0))) \rightsquigarrow_{\{x \mapsto S(S(0))\}}$$

Needed narrowing: demand-driven variable instantiation and rewriting

$$\text{leq}(x, \text{add}(0, S(0))) \rightsquigarrow_{\{x \mapsto 0\}} \text{True}$$

Sound, complete, optimal evaluation strategy [JACM'00]



ELIMINATING OVERLAPPING RULES

Transformation *OE*: OIS \longrightarrow ISX:

Replace overlapping rule $f(\overline{t_n}) \rightarrow r_1 ? \dots ? r_k$ by:

$f(\overline{t_n}) \rightarrow f'(y, \overline{x_l})$ (where $\mathcal{V}ar(\overline{t_n}) = \{x_1, \dots, x_l\}$, y fresh, f' new)

$f'(I_1, \overline{x_l}) \rightarrow r_1$

⋮

$f'(I_k, \overline{x_l}) \rightarrow r_k$

data $I_x = I_1 \mid \dots \mid I_k$ (index type, e.g., natural numbers)



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\vdots

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data $I_x = I_1 \mid \dots \mid I_k$ (index type, e.g., natural numbers)

Example: $\text{parent}(x) \rightarrow \text{mother}(x) ? \text{father}(x)$

OE \mapsto data $I_{\text{parent}} = I_0 \mid I_1$

$\text{parent}(x) \rightarrow \text{parent}'(y, x)$

$\text{parent}'(I_0, x) \rightarrow \text{mother}(x)$

$\text{parent}'(I_1, x) \rightarrow \text{father}(x)$



ELIMINATING OVERLAPPING RULES: RESULTS

Proposition: $\mathcal{R} \in \text{OIS} \Rightarrow OE(\mathcal{R}) \in \text{ISX}$



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Correctness w.r.t. results computed by needed narrowing:

Theorem: $\mathcal{R} \in \text{OIS}$, $\mathcal{R}' = OE(\mathcal{R})$, t, s terms of \mathcal{R} :

Soundness: $t \overset{\text{NN}^*}{\rightsquigarrow}_{\sigma'} s$ w.r.t. $\mathcal{R}' \Rightarrow \exists t \overset{\text{NN}^*}{\rightsquigarrow}_{\sigma} s$ w.r.t. \mathcal{R} with $\sigma =_{\text{var}(t)} \sigma'$

Completeness: $t \overset{\text{NN}^*}{\rightsquigarrow}_{\sigma} s$ w.r.t. $\mathcal{R} \Rightarrow \exists t \overset{\text{NN}^*}{\rightsquigarrow}_{\sigma'} s$ w.r.t. \mathcal{R}' with $\sigma =_{\text{var}(t)} \sigma'$



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\Rightarrow Implementations need not implement overlapping rules

(done in several implementations but without formal justification)



ELIMINATING LOGIC VARIABLES

Transformation $XE: ISX \longrightarrow OIS^-$

(extra variable elimination)

OIS^- : overlapping inductively sequential *without extra variables*

Evaluation in OIS^- : rewriting (not narrowing)

Thus: programs transformed by XE

\rightsquigarrow implementation without handling of logic variables and substitutions



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Basic idea of XE : replace extra variables in rules by **value generators**



VALUE GENERATORS

S : sort defined by datatype declaration

data $S = C_1 t_{11} \dots t_{1a_1} \mid \dots \mid C_n t_{n1} \dots t_{na_n}$

Value generator operation $\text{instOf } S$ defined by (overlapping) rules

$\text{instOf } S \rightarrow C_1(\text{instOf } t_{11}, \dots, \text{instOf } t_{1a_1})$
? ...
? $C_n(\text{instOf } t_{n1}, \dots, \text{instOf } t_{na_n})$



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Example: $\text{data TreeNat} = \text{Leaf} \mid \text{Branch Nat TreeNat TreeNat}$

$$\begin{aligned} \text{instOfTreeNat} &\rightarrow \text{Leaf} \\ &\quad ? \text{Branch}(\text{instOfNat}, \text{instOfTreeNat}, \text{instOfTreeNat}) \end{aligned}$$
$$\text{instOfTreeNat} \rightarrow 0 \quad ? \text{S}(\text{instOfNat})$$


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Lemma: \forall ground constructor terms t of sort S : $\text{instOf } S \xrightarrow{*} t$



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Lemma (Completeness of XE): $t \overset{*}{\rightsquigarrow} u$ w.r.t. $\mathcal{R} \Rightarrow$
 $XE(t) \overset{*}{\rightarrow} v$ w.r.t. $XE(\mathcal{R})$ (v : ground constructor instance of u)



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XE is not sound:

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Solution: identical reductions for extra variable generators



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Theorem: transformation *XEP* is sound and complete



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Initial goals with logic variables: t with $\mathcal{Var}(t) = \{x_1, \dots, x_n\}$

\rightsquigarrow start computation with initial term (t, x_1, \dots, x_n) :

Result (e, b_1, \dots, b_n) :

$e \approx$ computed value

$b_1, \dots, b_n \approx$ computed answer



CONCLUSIONS

Two transformations on functional logic programs:

1. eliminate overlapping rules by extra variables
 - any functional logic program can be mapped into ISX
 - ISX \approx core language for functional logic programming
 - practice: ISX already used as core in some implementations
2. eliminate extra variables by new operations (overlapping rules)
 - correctness requires admissible derivations
 - implement admissible derivations by sharing / let expressions



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Results useful for

- better understanding of functional logic programming features
- simpler core languages (support overlapping rules *or* extra variables)
- simpler implementations
- simplify tool support (e.g., current tracers, profilers, slicers, partial evaluators: core language with overlapping rules *and* extra variables)
- better understanding of the role of logic variables

